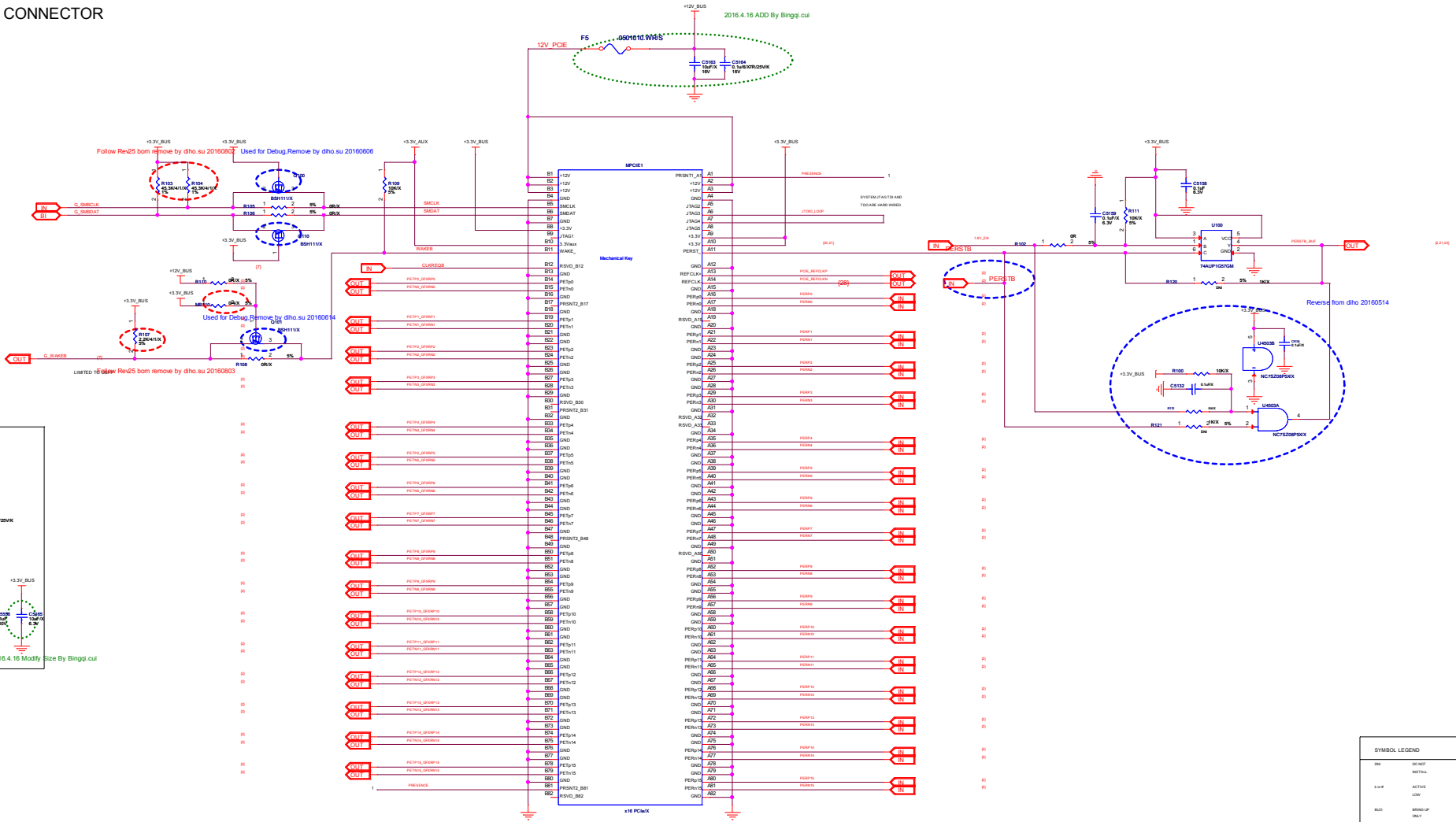
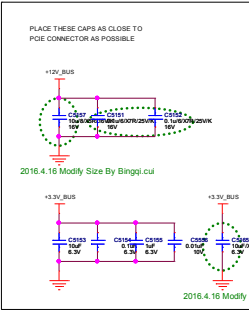
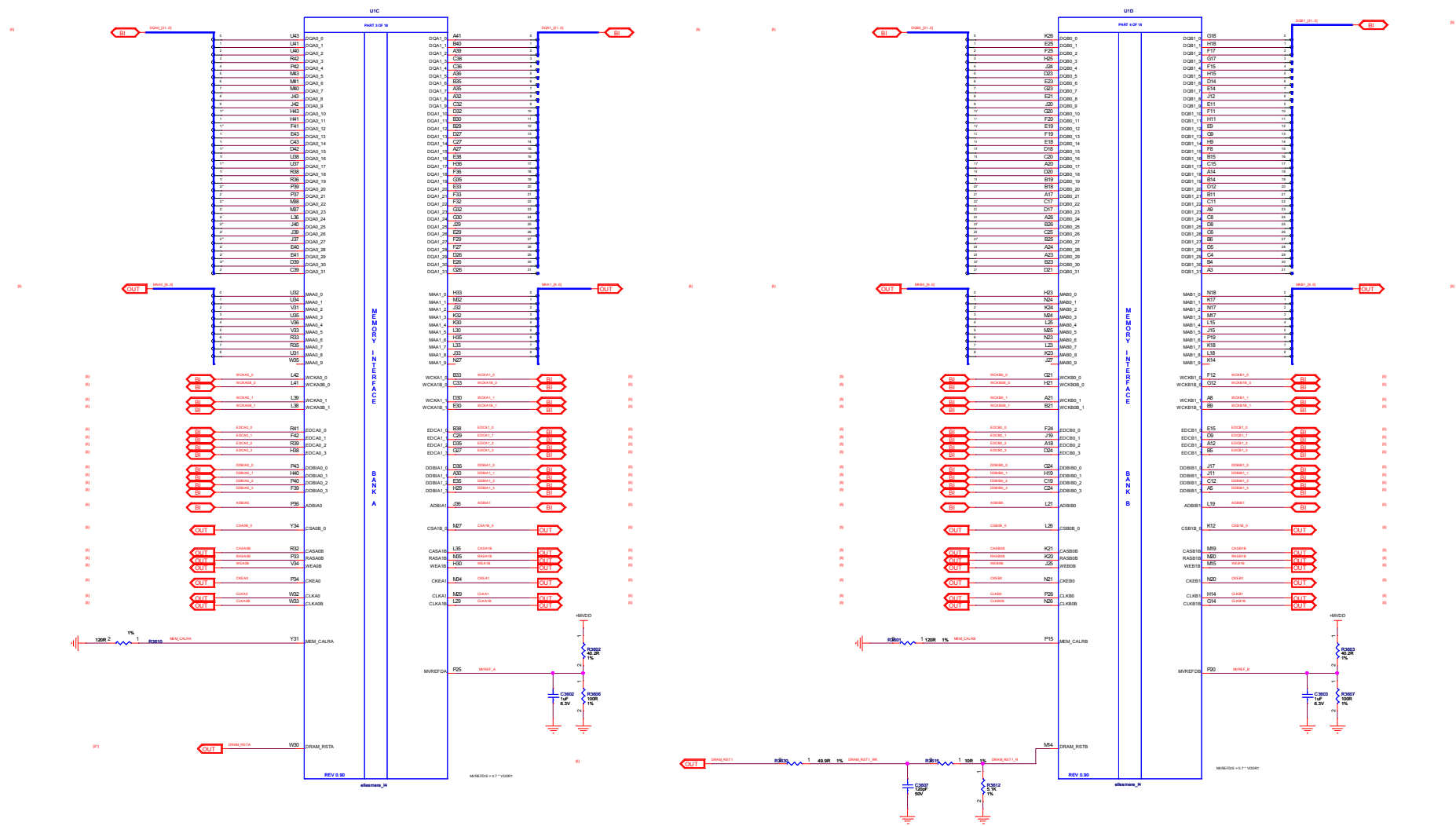


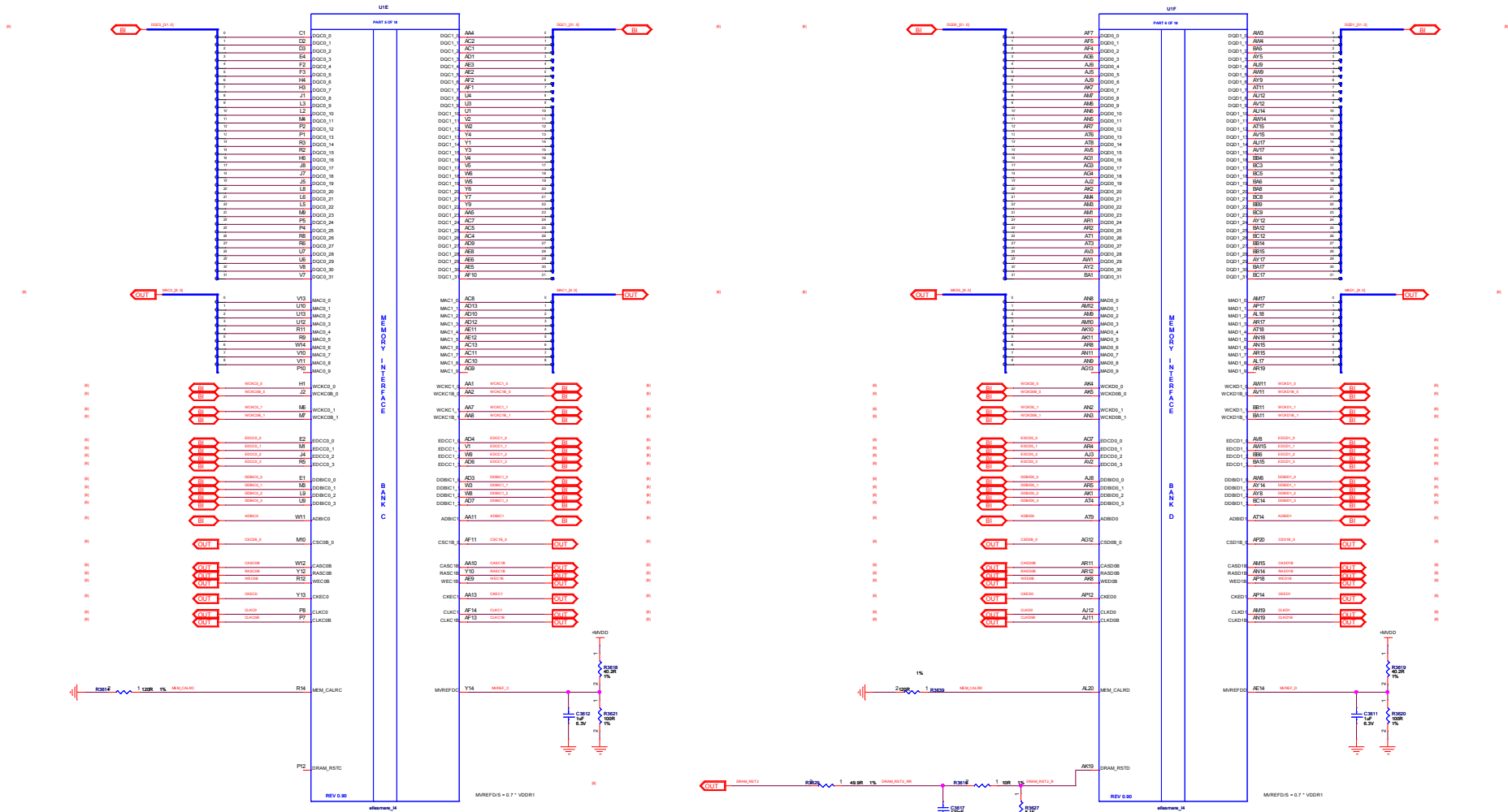
(1) PCI-EXPRESS EDGE CONNECTOR



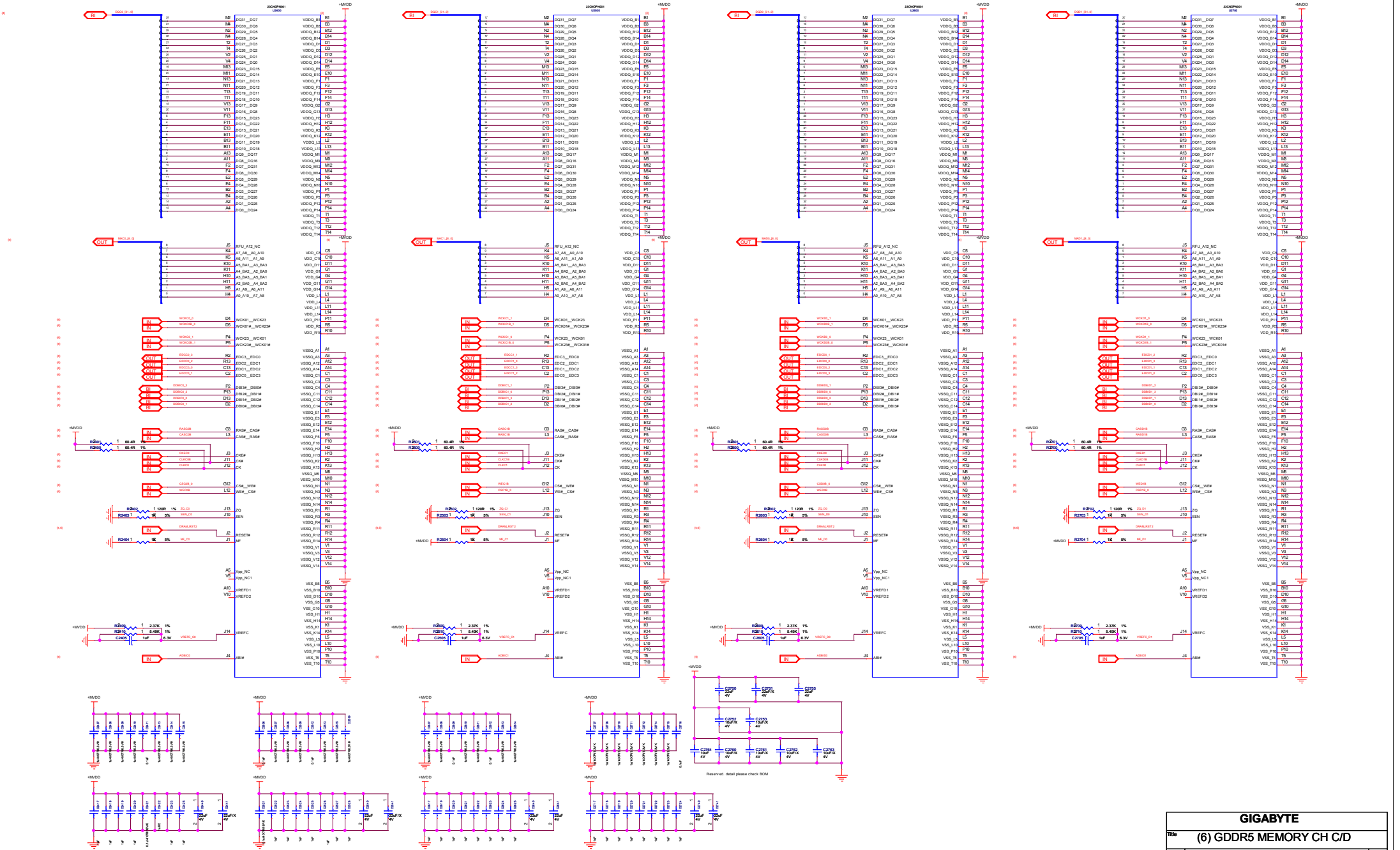
(3) ELLESMERE MEM INTERFACE CH A/B



(4) ELLESMERE MEM INTERFACE CH C/D



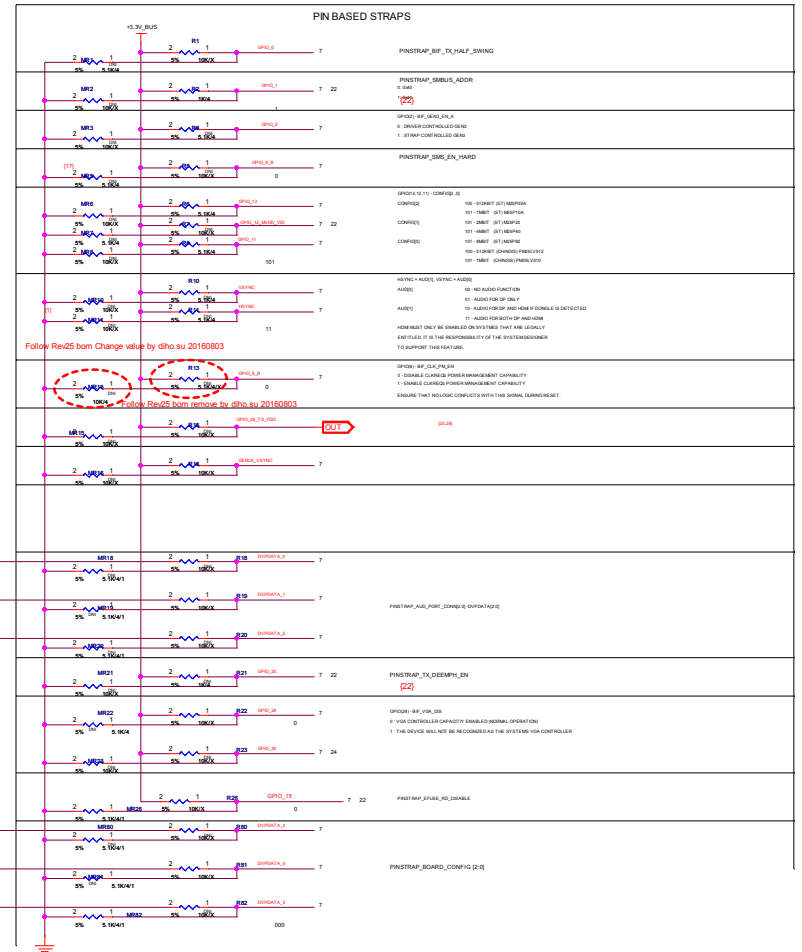
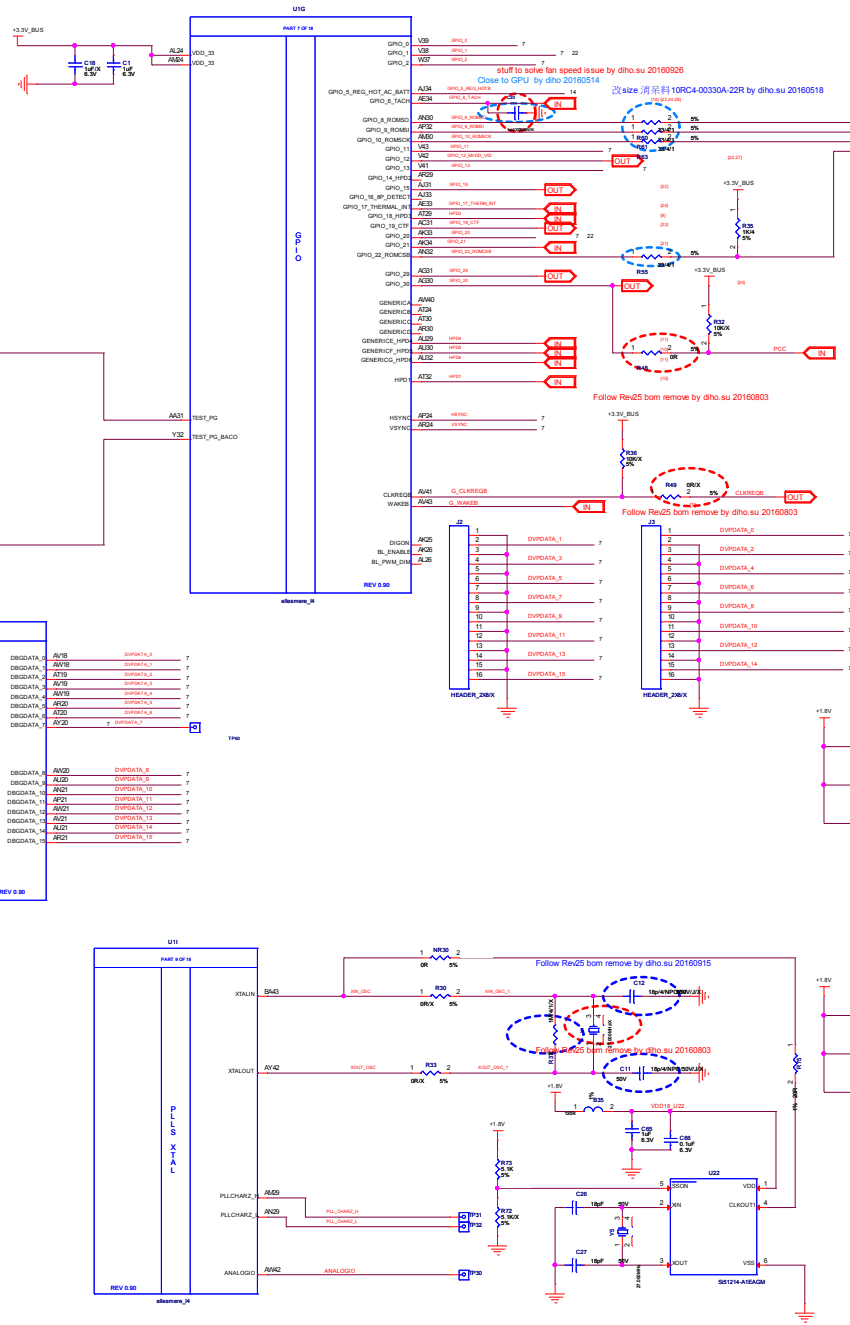
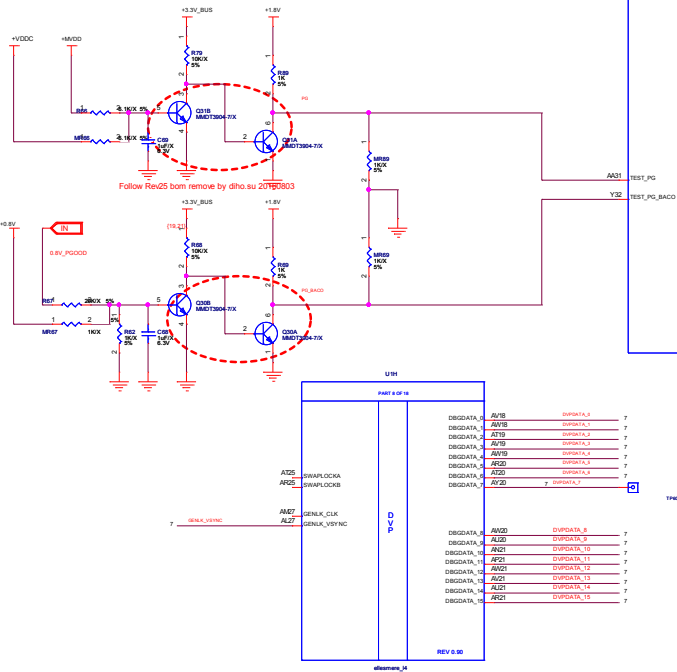
(6) GDDR5 MEMORY CH C/D



(7) ELLESMERE GPIO STRAP CF XTAL

I2C/SDA BUS:		
I2C ADDRESS	FUNCTION	DEVICE

I2C ADDRESS	FUNCTION	DEVICE
0x98	EXT TEMP SENSOR	LM6003



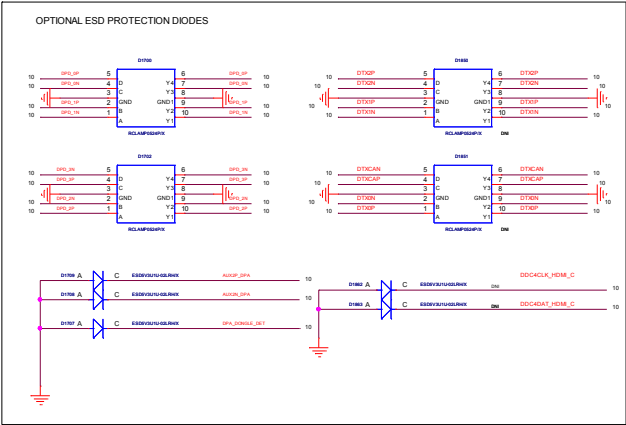
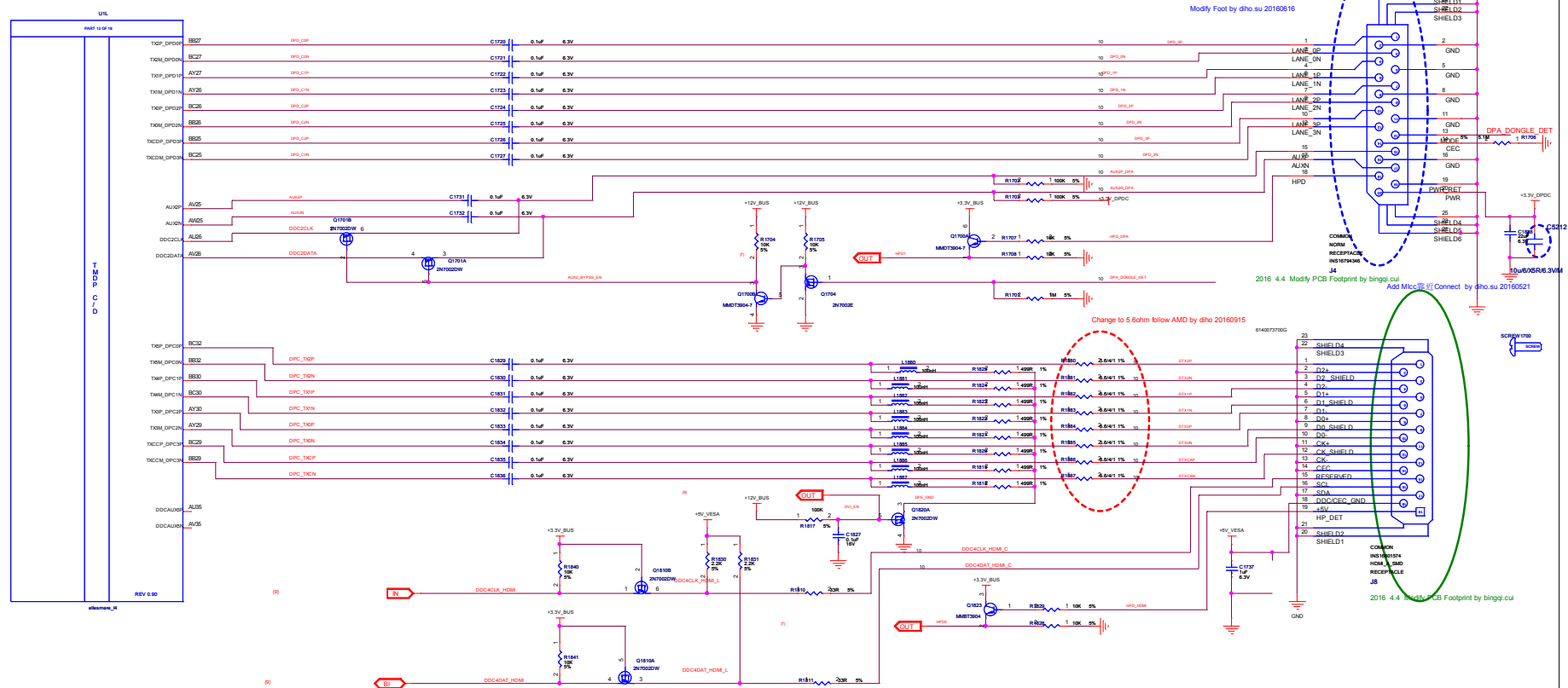
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GIGABYTE			
Title: ELLESMERE GPIO STRAP CF XTAL			
Size Custom	Document Number GV-RX570GAMING-4GD		Rev 1.0
Date:	Wednesday, March 01, 2017	Sheet 7 of 29	

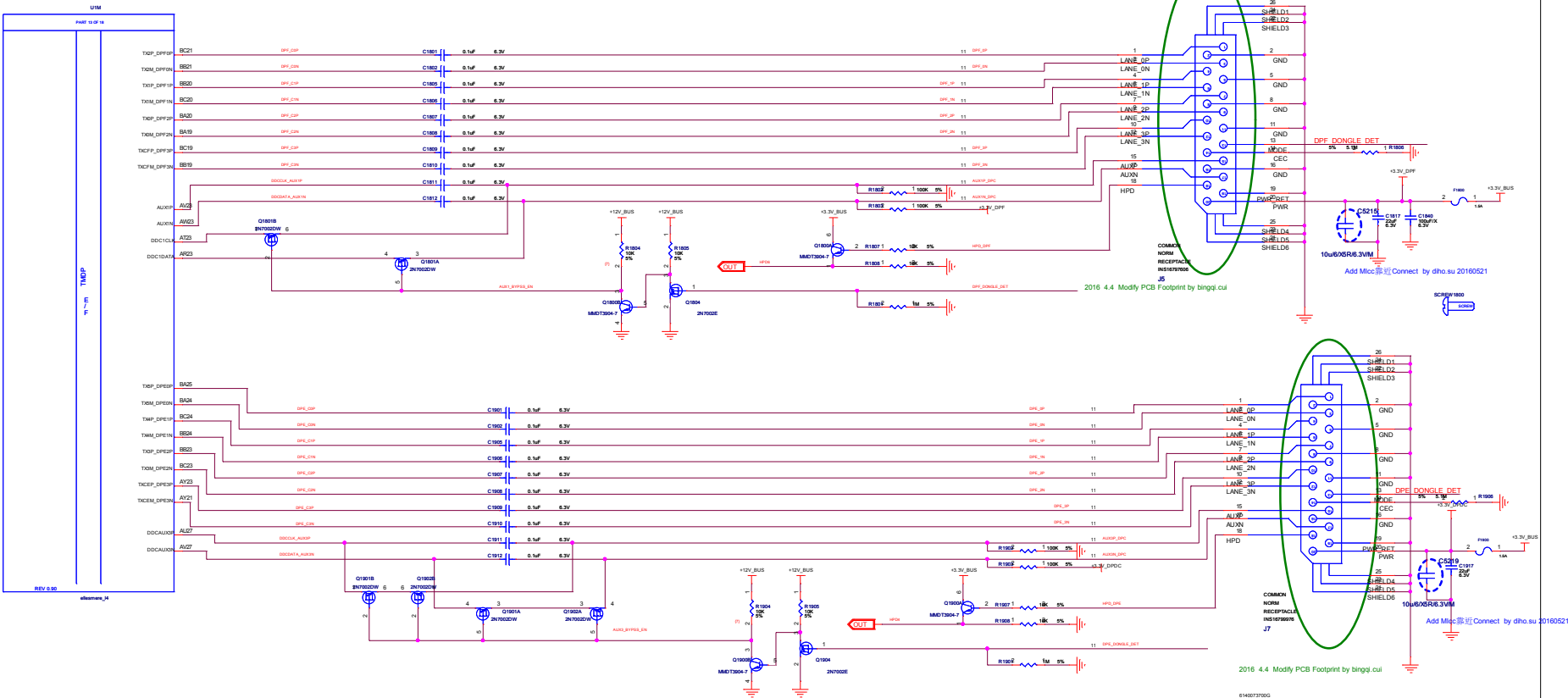
(8) ELLESMERE DAC1 LOCK



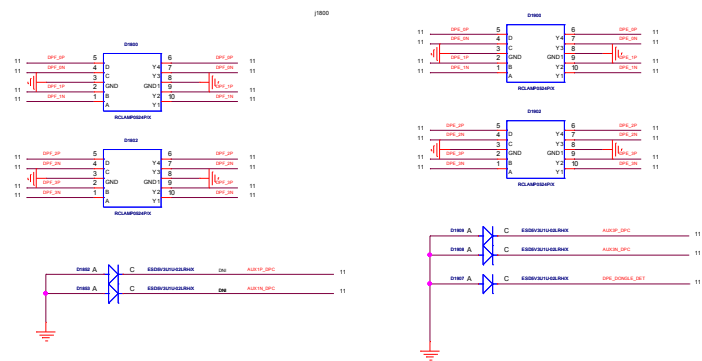
GIGABYTE			
Title ELLESMERE DAC1 LOCK			
Size	Document Number		Rev
Custom	GV-RX570GAMING-4GD		1.0
Date:	Wednesday, March 01, 2017	Sheet	8 of 29



GIGABYTE			
ELLESMERE TMDP C/D			
Size	Document Number	Rev	
Custom	GV-RX570GAMING-4GD	1.0	
Date:	Wednesday, March 01, 2017	Sheet	10 of 20

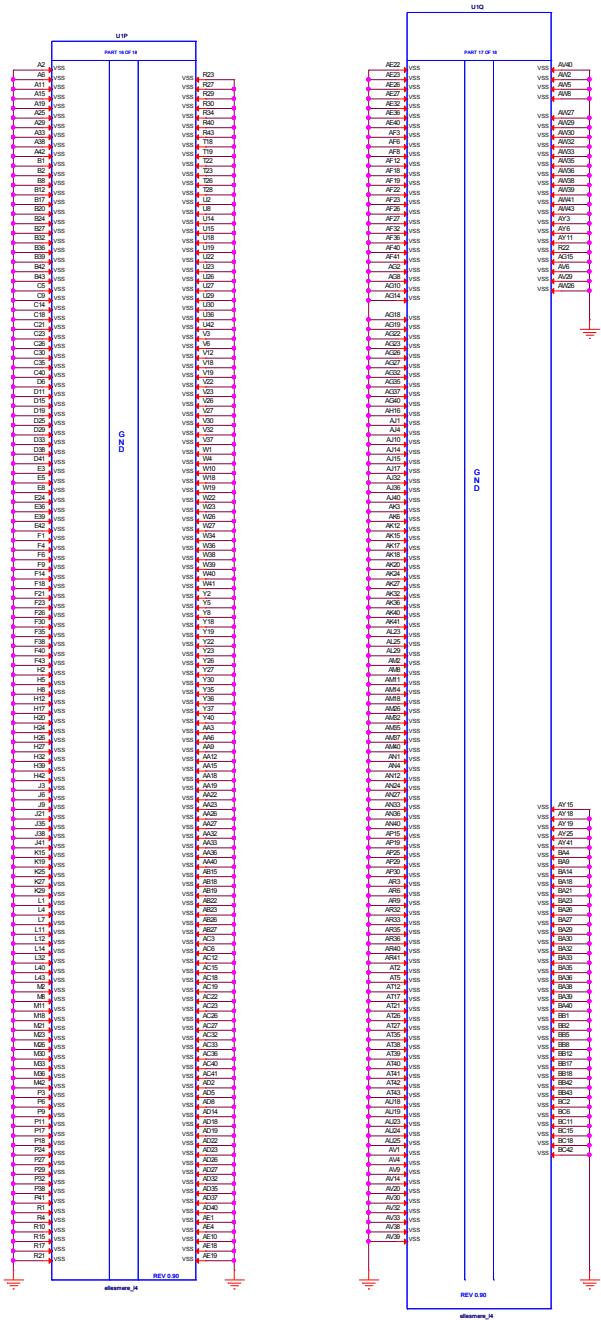


OPTIONAL ESD PROTECTION DIODES

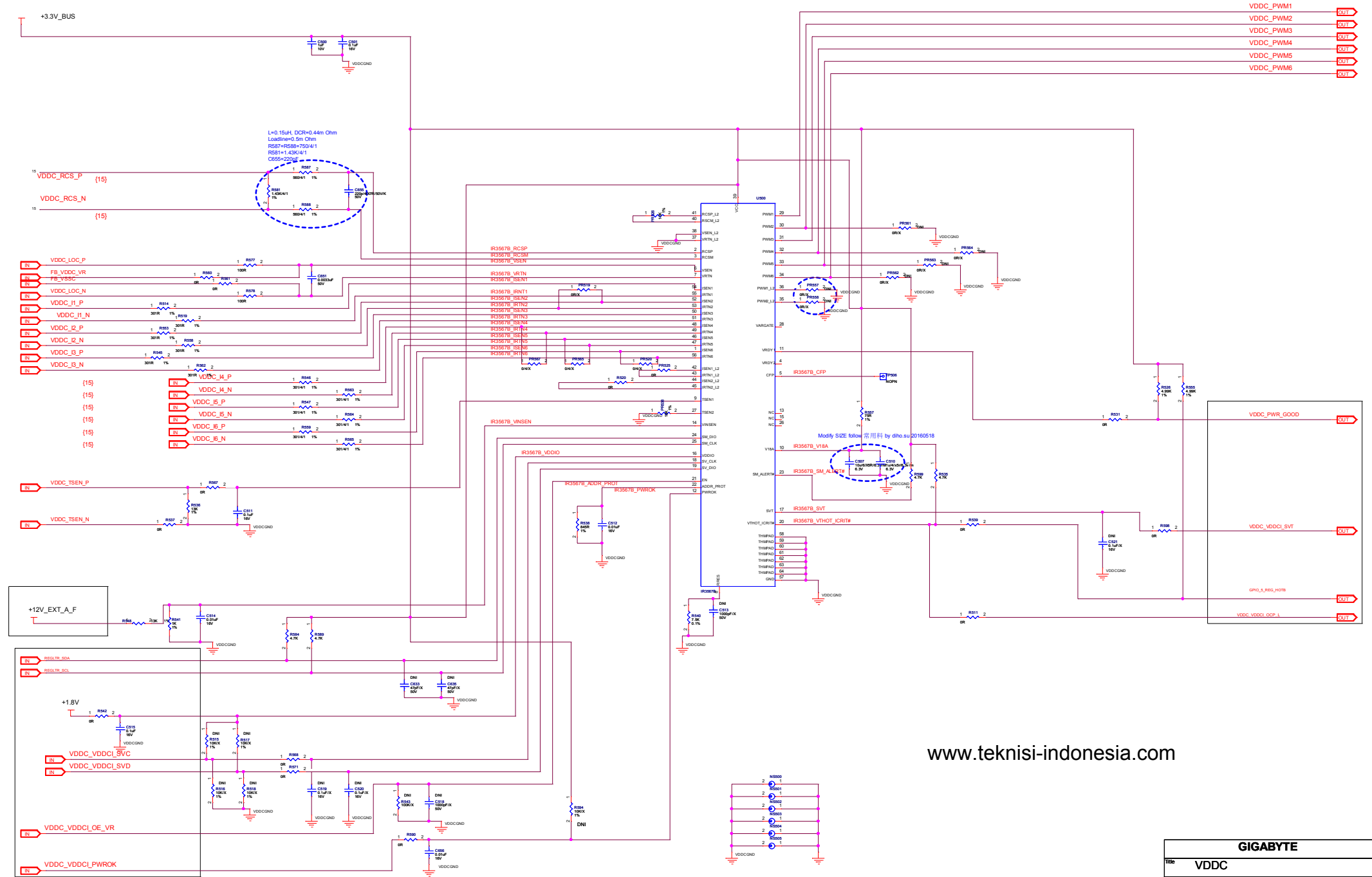


GIGABYTE			
ELLESMERE LVTMDP E/F			
Title			
Size	Document Number		Rev
Custom	GV-RX570GAMING-4GD		1.0
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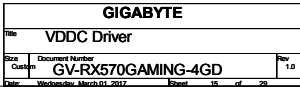
(13) ELLESMERE GROUND

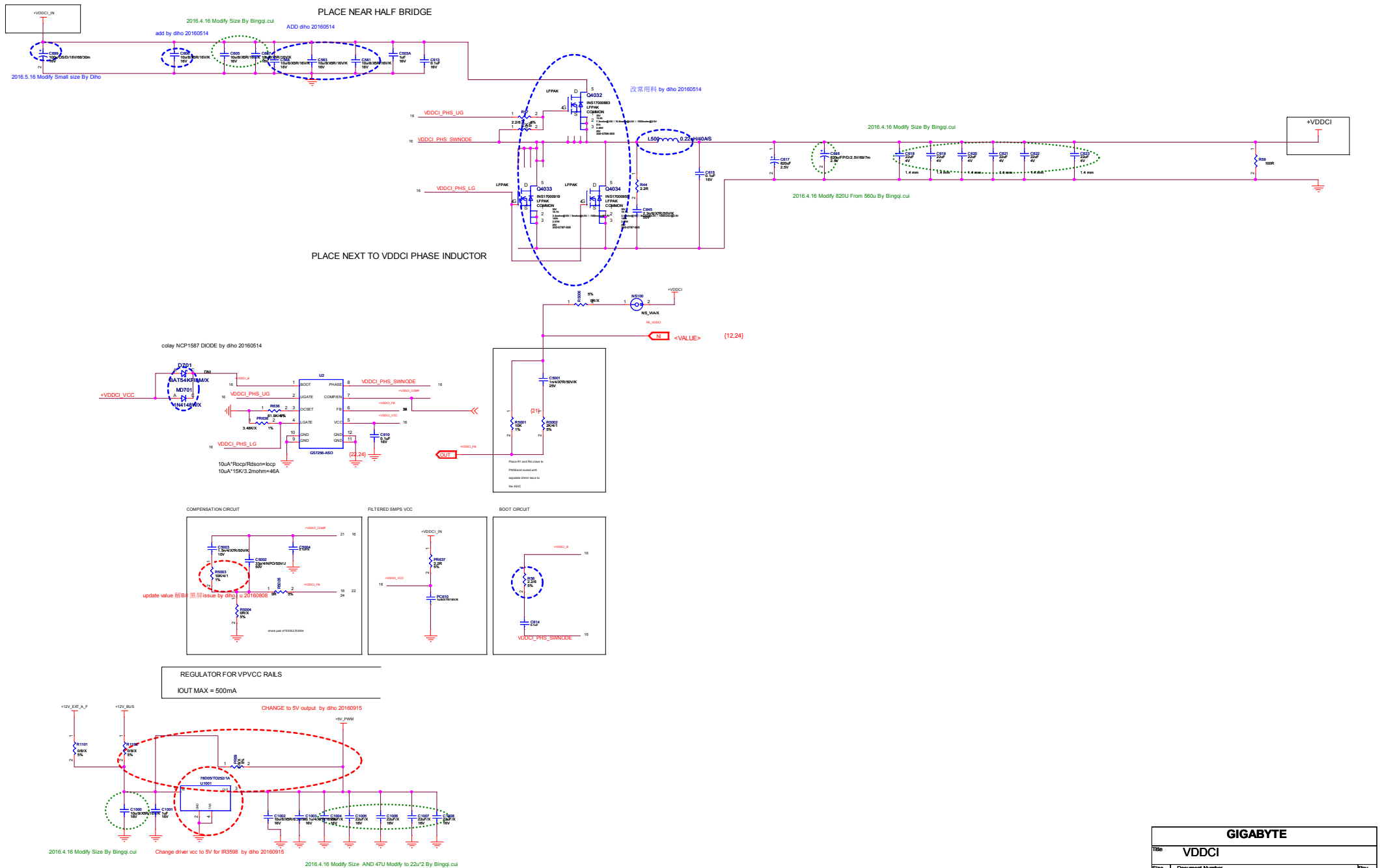


GIGABYTE			
ELLESMERE GND			
Size	Document Number	Rev	
Customer	GV-RX570GAMING-4GD	1.0	
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GIGABYTE			
Site	Document Number	Rev	
Customer	GV-RX570GAMING-4GD	1.0	
Date:	Wednesday, March 01, 2017	Sheet	14 of 29



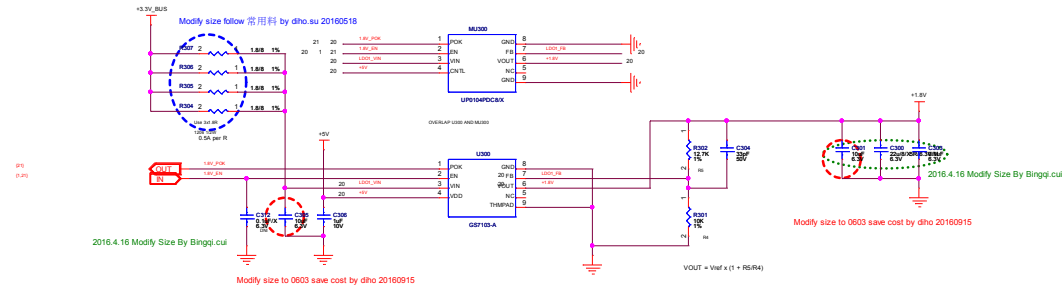


GIGABYTE			
Title	VDDCI		
Size	Document Number	Rev	
Created	GV-RX570GAMING-4GD	1.0	
Date	Wednesday, March 01, 2017	Sheet	16 of 29

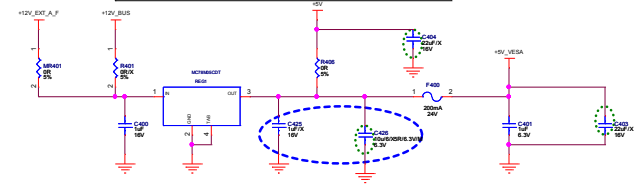


(18) SMALL RAIL REGULATORS

LDO #1: VIN = 3.0V TO 3.6V MAX
VOUT = +1.8V +/- 2%
IOU = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING

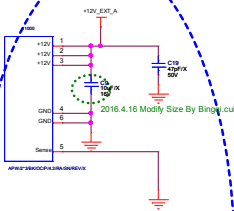


REGULATOR FOR +5V RAILS
IOU MAX = 150mA

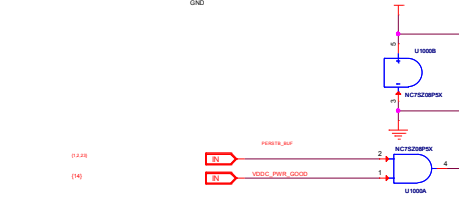
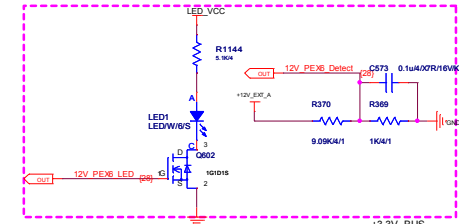
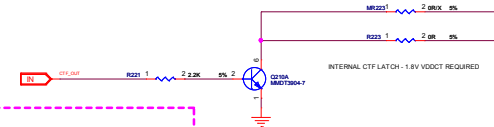
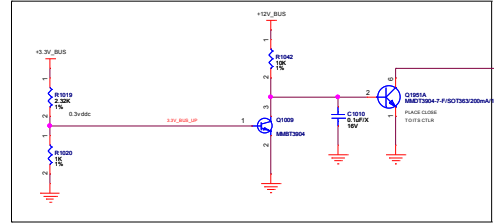
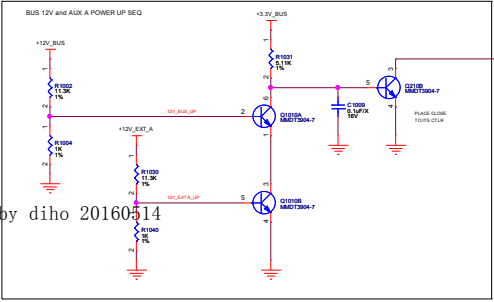
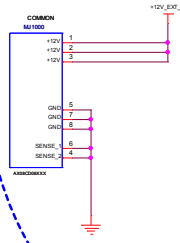


GIGABYTE			
Title SMALL RAIL REGULATORS			
Size	Document Number	Rev	
Custom	GV-RX570GAMING-4GD	1.0	
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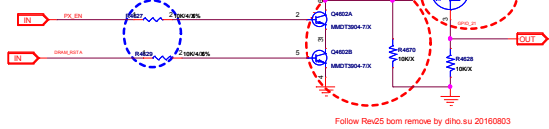
(19) POWER MANAGEMENT



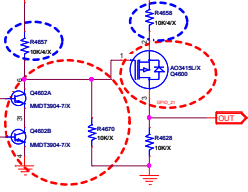
6PIN&8PIN CONNECT COLAY by diho 20160514



Follow Rev25 bom remove by diho.su 20160915

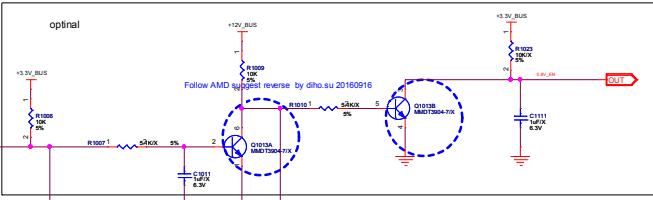
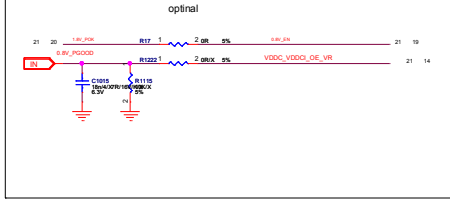


Follow Rev25 bom remove by diho.su 20160915



Follow Rev25 bom remove by diho.su 20160803

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Follow AMD suggest reverse by diho.su 20160916

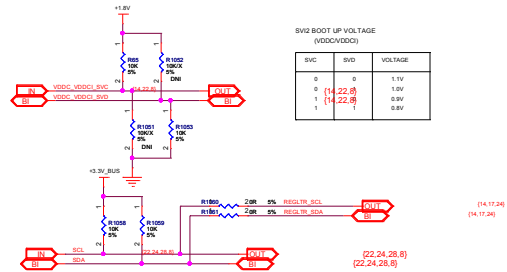
Follow AMD suggest reverse by diho.su 20160915

Follow AMD suggest reverse by diho.su 20160915

Follow Rev25 bom remove by diho.su 20160803

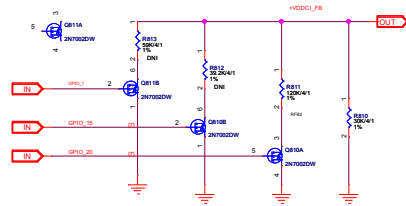
(14,22,8)

(22,24,28,8)

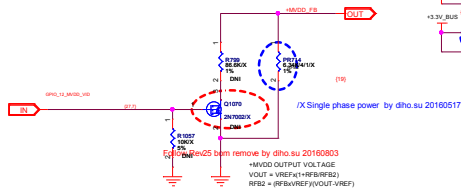


(14,17,24)

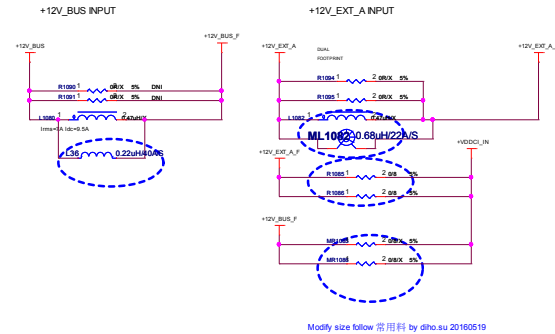
(22,24,28,8)



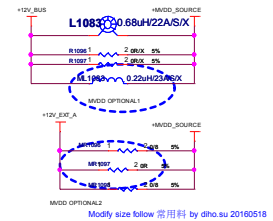
(14,24)



Follow Rev25 bom change vdd_06 to 0.9V by dtho.su 20160803

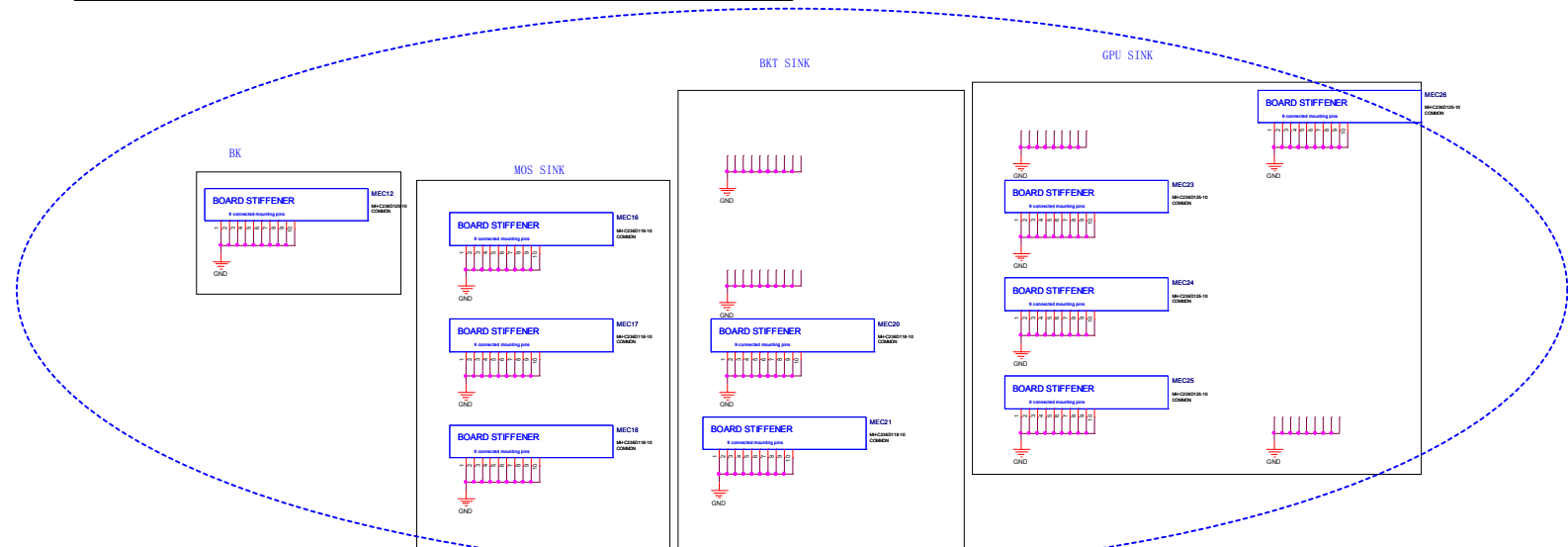
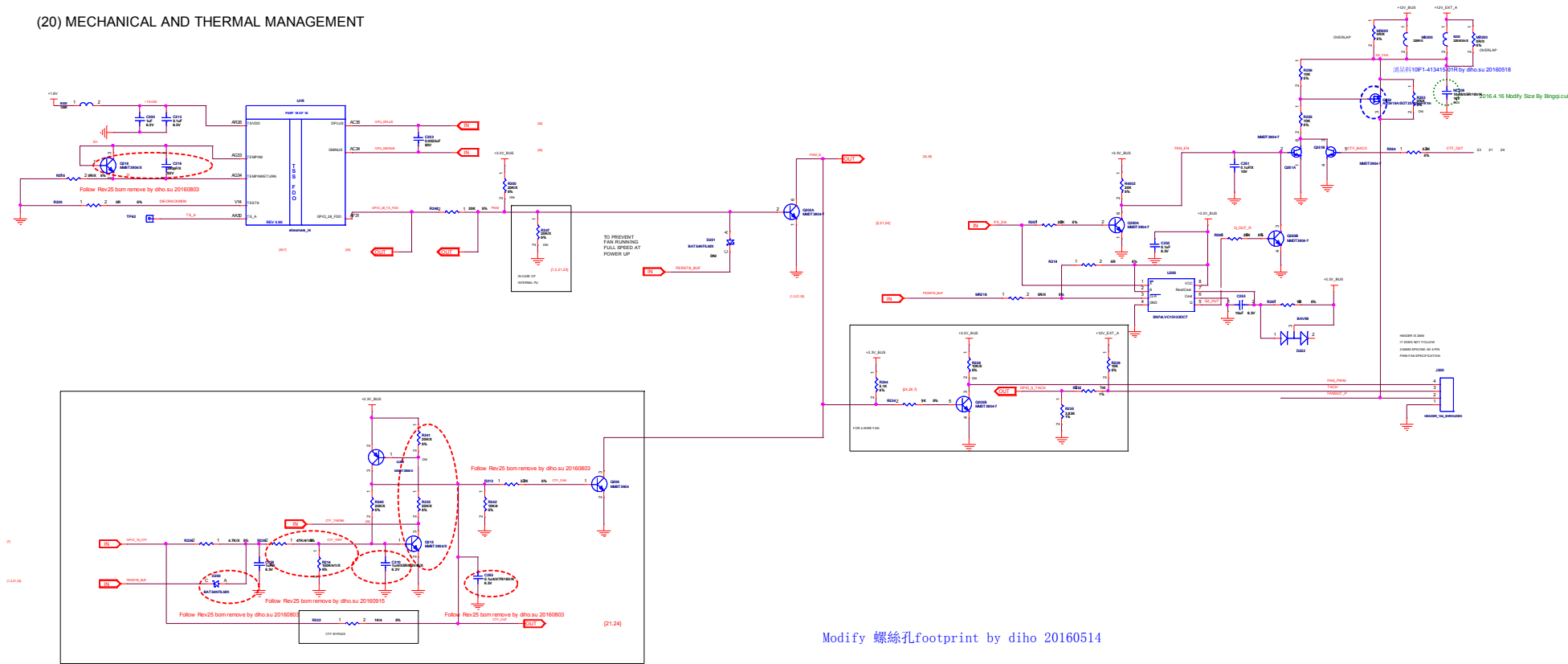


Modify size follow 常用料 by dtho.su 20160519

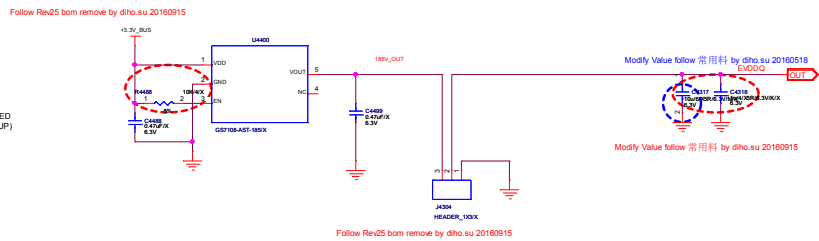
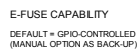
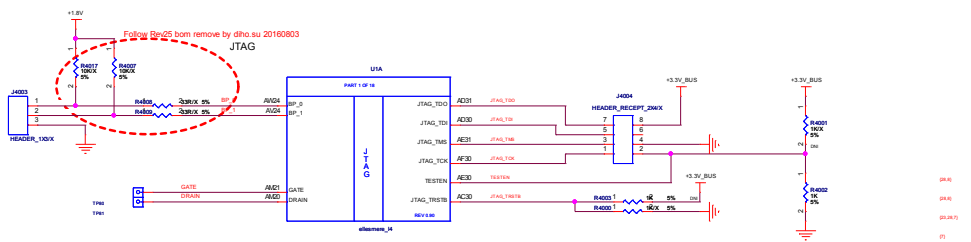


Modify size follow 常用料 by dtho.su 20160518

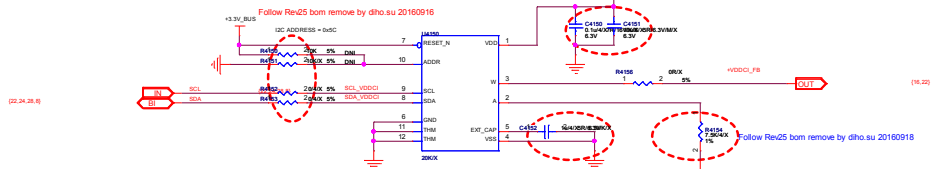
(20) MECHANICAL AND THERMAL MANAGEMENT



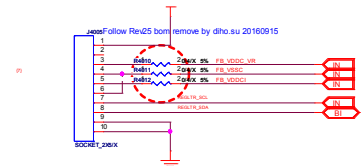
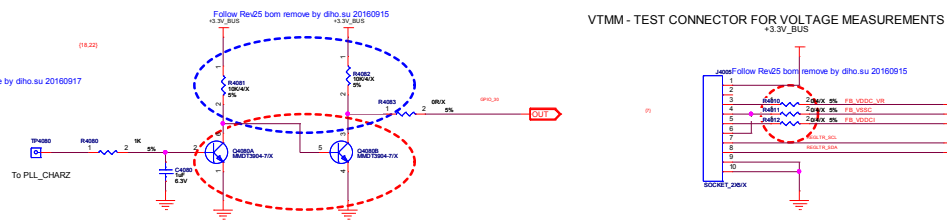
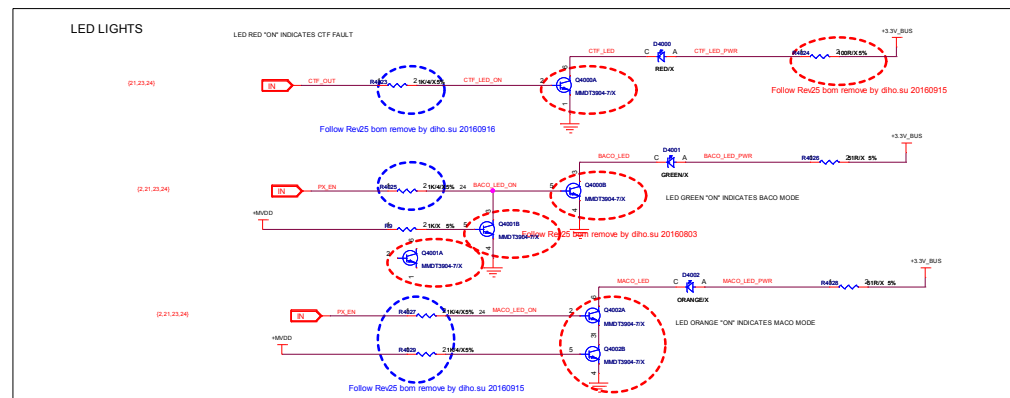
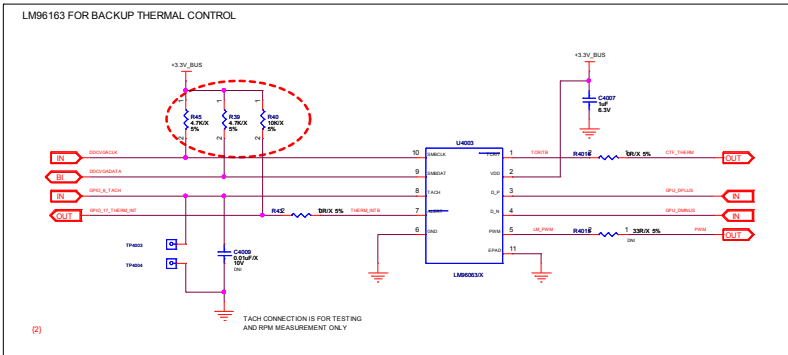
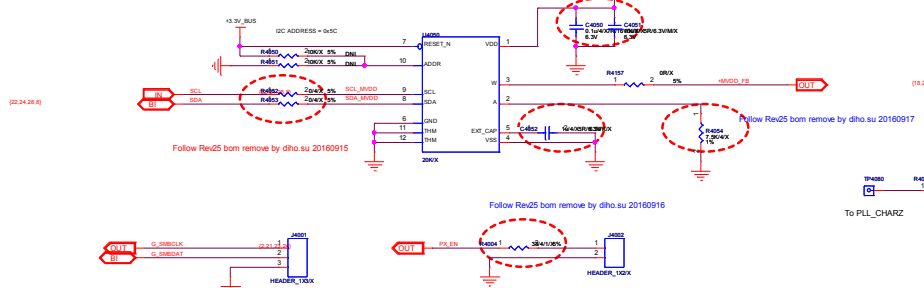
(21) DEBUG CIRCUITS



DIGITAL POTS

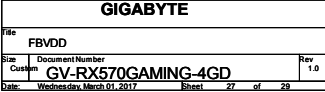


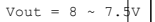
DIGITAL POTS



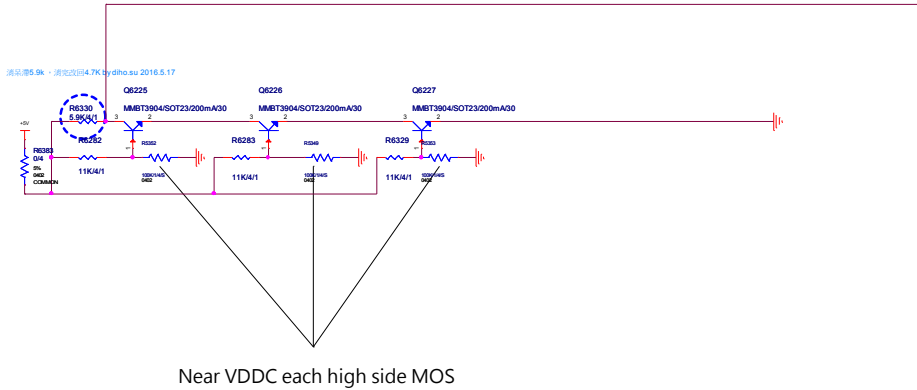
GIGABYTE			
Title DEBUG CIRCUITS			
Size	Document Number		Rev
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4	00A	00000000	
1	00B	00000000	1. Add SP017 to operation 10000 2. update 10000 to add response
2	00C	00000000	PC000: - add series resistor R11000 - R11007 - add pull-down inductor L11000 - L11007 remove C4000, C4003, VR4000, C4100, C410A, R4000
3	00D	00000000	Modify 10017 Design

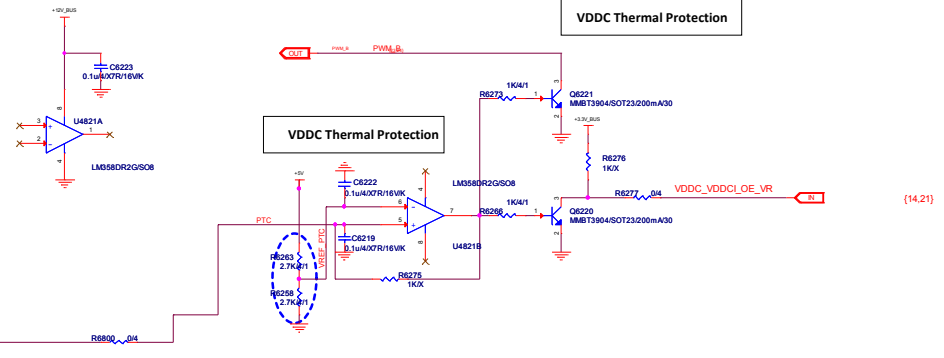




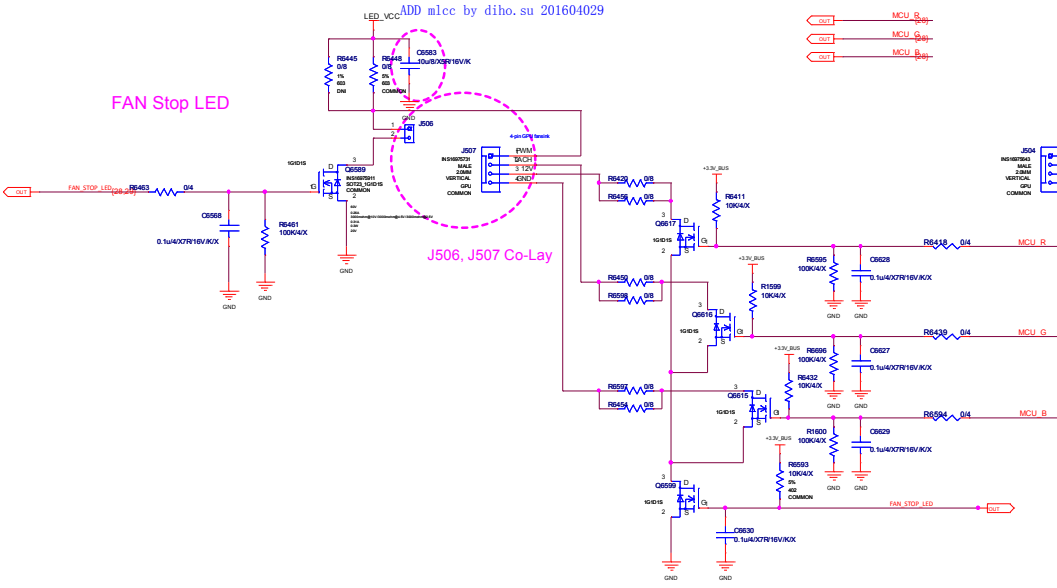
Add MOS VRHOT CIRCUIT



Near VDDC each high side MOS



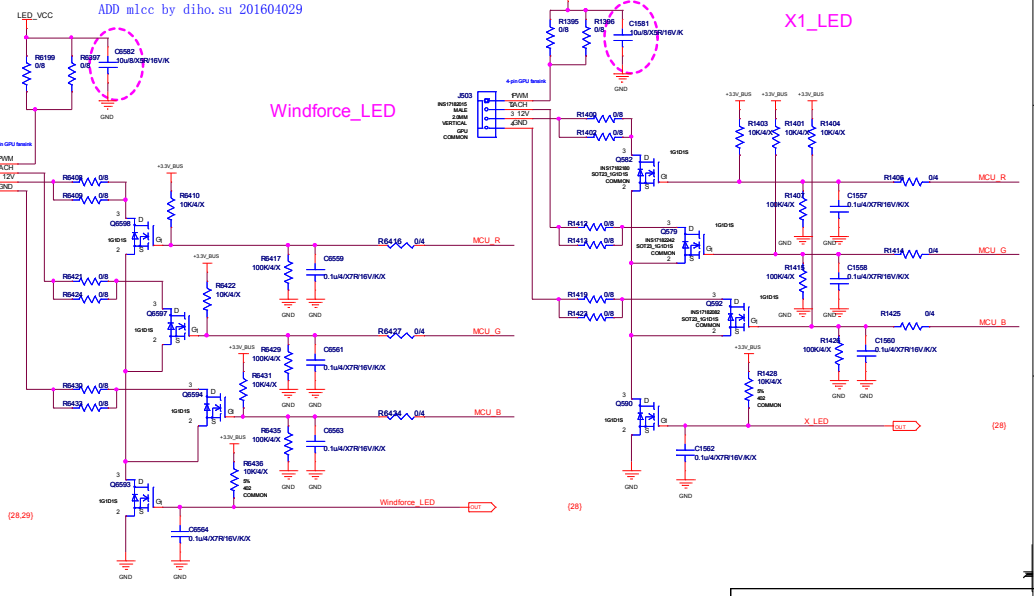
ADD mlcc by diho.su 201604029



FAN Stop LED

J506, J507 Co-Lay

ADD mlcc by diho.su 201604029



Windforce_LED

X1_LED

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File		
Site	Document Number	Rev
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